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Relevance scale ☐ ☐ ☐ ☐ ☐1 [A comparative study of static and profile-based heuristics for inlining](#)

Matthew Arnold, Stephen Fink, Vivek Sarkar, Peter F. Sweeney

 January 2000 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN workshop on Dynamic and adaptive compilation and optimization DYNAMO '00**, Volume 35 Issue 7

Publisher: ACM Press

Full text available: [pdf\(1.13 MB\)](#)
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In this paper, we present a comparative study of static and profile-based heuristics for inlining. Our motivation for this study is to use the results to design the best inlining algorithm that we can for the Jalapeño dynamic optimizing compiler for Java [6]. We use a well-known approximation algorithm for the KNAPSACK problem as a common "meta-algorithm" for the inlining heuristics studied in this paper. We present performance results for an implementation of these inlinin ...

2 [Design and evaluation of dynamic optimizations for a Java just-in-time compiler](#)

Toshio Sukanuma, Toshiaki Yasue, Motohiro Kawahito, Hideaki Komatsu, Toshio Nakatani

 July 2005 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 27 Issue 4

Publisher: ACM Press

Full text available: [pdf\(1.60 MB\)](#)
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The high performance implementation of Java Virtual Machines (JVM) and Just-In-Time (JIT) compilers is directed toward employing a dynamic compilation system on the basis of online runtime profile information. The trade-off between the compilation overhead and performance benefit is a crucial issue for such a system. This article describes the design and implementation of a dynamic optimization framework in a production-level Java JIT compiler, together with two techniques for profile-directed o ...

**Keywords:** JIT compiler, Recompilation, adaptive optimization, code specialization, dynamic compilation, profile-directed method inlining

3 [When to use a compilation service?](#)

Jeffrey Palm, Han Lee, Amer Diwan, J. Eliot B. Moss

 June 2002 **ACM SIGPLAN Notices , Proceedings of the joint conference on Languages, compilers and tools for embedded systems: software and compilers for**

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